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APPLICANTS

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** CONTINUING DATA ***** *RCE* *SK*
 This application is a DIV of 09/925,628 08/10/2001 PAT 6,690,070

** FOREIGN APPLICATIONS ***** *YLS* *SK*
 JAPAN 2000-242617 08/10/2000

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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>SK (2/28/06)</i> Examiner's Signature Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 3	TOTAL CLAIMS 6	INDEPENDENT CLAIMS 2
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TITLE
 Method of manufacturing insulated gate semiconductor device

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